



XA-9582
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yoshihiro IKEDA et al.

Appln. No.: 09/987,884

Group Art Unit: 2813

Filed: November 16, 2001

Examiner: L. Schillinger

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

It is respectfully requested that the time now set for response be extended, retroactively, by three months. The required petition fee for a large entity is submitted herewith. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper to Deposit Account No. 50-1165.

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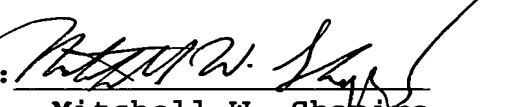
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Respectfully submitted,

MWS:sjk

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May 29, 2003

By: 
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